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MICROELECTRONIC CIRCUIT DESIGN. FIFTH EDITION Richard C. Jaeger Distinguished University Professor Emeritus ECE Department Auburn University jaegerc@auburn.edu and Travis N. Blalock Visiting Associate Professor ECE Department University of Virginia blalock@virginia.edu

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(2.19) can be used to evaluate A_{cm} of the second stage.) D 2.76 Design the instrumentation-amplifier circuit of Fig. 2.20(b) to realize a differential gain, variable in the range 2 to 100, utilizing a 100-k pot as variable resistor. *2.77 The circuit shown in Fig. P2.77 is intended to supply a voltage to floating loads (those for which both ...

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inputs that produces a single binary output. Depending on the context, the term may refer to an ideal logic gate, one that has for instance zero rise time and unlimited fan-out, or it may refer to a non-ideal physical device (see ...

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Marc T. Thompson Ph.D., in *Intuitive Analog Circuit Design (Second Edition)*, 2014 Bessel filter The Bessel filter (sometimes called the "Thomson" filter) is optimized to provide a constant group delay in the filter passband, while sacrificing sharpness in the magnitude response.

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