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The design was complete by 1970, and used a MOS-based chipset as the core CPU. The design was significantly (approximately 20 times) smaller and much more reliable than the mechanical systems it competed against, and was used in all of the early Tomcat models. This system contained "a 20-bit, pipelined, parallel multi-microprocessor". The Navy ...

[SDARC - Wikipedia](#)

Earlier this month, Intel introduced first budget dual- and quad-core systems on a chip, built on "Gemini Lake" design. As you may know, previous generation of processors for inexpensive desktop and notebook computers was based on "Apollo Lake" design, which was produced on 14nm technology, and combined 2 or 4 "Goldmont" CPU cores, Intel HD 500 or HD 505 graphics, DDR3 / DDR4 memory controller ...