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[Q. 3.18: Draw a logic diagram using only two-input NOR gates to implement the following function:](#)

Q. 3.18: Draw a logic diagram using only two-input NOR gates to implement the following function: by Dr. Dhiman Kakati 1 year ago 12 minutes, 17 seconds 7,370 views Q. 3.18: Draw a , logic , diagram using only two-input NOR gates to implement the following function: $F(A, B, C, ...)$

[Q. 4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD](#)

Q. 4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD by Dr. Dhiman Kakati 11 months ago 12 minutes, 20 seconds 7,645 views Q. 4.8: Design , a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD (see Table 1.5).

[3.19: Simplify the following functions, and implement them with two-level NOR gate circuits:](#)

3.19: Simplify the following functions, and implement them with two-level NOR gate circuits: by Dr. Dhiman Kakati 1 year ago 13 minutes, 21 seconds 7,572 views 3.19: Simplify the following functions, and implement them with two-level NOR gate circuits: (a) $F = wx' + y'z'$...

[Q. 2.17: Obtain the truth table of the following functions, and express each function in sum-of-min-](#)

Q. 2.17: Obtain the truth table of the following functions, and express each function in sum-of-min- by Dr. Dhiman Kakati 1 year ago 15 minutes 8,410 views Q. 2.17: Obtain the truth table of the following functions, and express each function in sum-of-min- terms and ...

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[Q. 7.15: Using 64 * 8 ROM chips with an enable input, construct a 512 * 8 ROM with eight chips and a](#)

Q. 7.15: Using 64 * 8 ROM chips with an enable input, construct a 512 * 8 ROM with eight chips and a by Dr. Dhiman Kakati 5 months ago 12 minutes, 17 seconds 1,021 views Q. 7.15: Using 64 * 8 ROM chips with an enable input, construct a 512 * 8 ROM with eight chips and a ...

[Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is](#)

Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is by Dr. Dhiman Kakati 8 months ago 16 minutes 10,104 views Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is specified by ...

[Q. 2.14: Implement the Boolean function \$F = xy + x'y' + y'z\$ \(a\) With AND, OR, and inverter gates](#)

Q. 2.14: Implement the Boolean function $F=xy + x'y' + y'z$ (a) With AND, OR, and inverter gates by Dr. Dhiman Kakati 1 year ago 13 minutes, 7 seconds 11,340 views Q. 2.14: Implement the Boolean function $F=xy + x'y' + y'z$ (a) With AND, OR, and inverter gates (b) With OR ...

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